

AMENDMENTS TO THE CLAIMS

1-16. (Canceled)

17. (New) A display device comprising:

a matrix of pixels, a pixel of said matrix of pixels having an electro-optic material between a pixel electrode and a common electrode;

a common driver having an offset circuit, a common voltage generated by said common driver being applied to said common electrode,

wherein said offset circuit is charged to an offset voltage at a time of a rising edge of a power supply voltage, said offset voltage adjusting a level of said common voltage.

18. (New) The display device as claimed in claim 17, wherein said offset circuit is discharged at a time of a falling edge of said power supply voltage.

19. (New) The display device as claimed in claim 18, wherein said matrix of pixels, said offset circuit, and said start circuit are mounted on an insulating substrate.

20. (New) The display device as claimed in claim 19, wherein said coupling capacitor is mounted on another substrate other than said insulating substrate.

21. (New) The display device as claimed in claim 18, wherein said common driver has a start circuit, said start circuit charging a coupling capacitor within said offset circuit to said offset voltage.

22. (New) The display device as claimed in claim 21, wherein said start circuit operates only at the time of the rising edge of the power supply voltage and at the time of a falling edge of the power supply voltage, said start circuit being in a non-operational state during other times.

23. (New) The display device as claimed in claim 17, further comprising:

a vertical driver connected to gate lines of said matrix of pixels;

a horizontal driver connected to signal lines of said matrix of pixels, said horizontal driver writing a signal voltage to said pixel electrode according to display data.

24. (New) The display device as claimed in claim 23, wherein said level of said common voltage is adjusted with respect to said signal voltage.

25. (New) The display device as claimed in claim 23, wherein said pixel of said matrix of pixels is located at an intersection of one of the gate lines and one of the signal lines.

26. (New) An electronic device capable of switching between a normal power consumption state and a low power consumption state, the electronic device comprising:

the display device as claimed in claim 17, the matrix of pixels being within a display area; and

a panel having a peripheral circuit and said display area, said panel being switchable between an operational mode and a standby mode;

wherein power consumption by said panel in said operational mode is higher than in said standby mode, driving of said display area being prohibited in said standby mode.